IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Harold S. Gross

§ GROUP ART UNIT: 1765

SERIAL NO.: 10/006,306

8 EXAMINER: Unknown

FILED: December 6, 2001

LAYERS AND **OXIDE THERMAL** FOR: THICK ISOLATION REGIONS IN A SILICON-CONTAINING

§ Attorney Docket No.:

SUBSTRATE FOR HIGH VOLTAGE APPLICATIONS

AM-5846

Date: May 31, 2002(1)

INFORMATION DISCLOSURE STATEMENT

TRANSMITTAL LETTER

Hon. Commissioner for Patents Washington, DC 20231

Sir:

Applicants are submitting the subject Information Disclosure Statement under 37 CFR § 1.97(b)(3). This Information Disclosure Statement is being submitted prior to the first Office Action in the subject patent application.

CERTIFICATE OF MAILING UNDER 37 CFR § 1.10

I hereby certify that this paper is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as U.S. EXPRESS MAIL NO. EU016348853US in an envelope addressed to the: Commissioner for Patents, Box DD, Washington, DC 20231.

Date: May 31, 2002

Shirley L. Church, Reg. No. 31,858

Applicants do not believe that any fee is due in connection with the filing of this Information Disclosure Statement under 37 CFR § 1.97(b)(3). However, in the event that any additional fee is due, the Commissioner is hereby authorized to charge Deposit Account No. <u>50-1512</u> of Shirley L. Church, Sunnyvale, California, in the amount of such fee.

This transmittal letter is submitted in duplicate for accounting purposes.

Respectfully submitted,

Shirley L. Church

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